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Substitute for form 1449/PTO				Complete if Known	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)				Application Number	10/724 280
				Filing Date	Herewith- 11-28-03
				First Named Inventor	Hocevar
				Art Unit	2133
				Examiner Name	
Sheet	1	of	1	Attorney Docket Number	TI-35252

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
S-AC		GALLAGER, <u>Low-Density Parity-Check Codes</u> , (MIT Press, 1963).	
S-A-C		MACKAY et al., "Comparison of Contructions of Irregular Gallager Codes", <u>Trans. Comm.</u> , Vol. 47, No. 10 (IEEE, Oct. 1999), pp. 1449-54.	
S-AC		TANNER et al., "A Class of Group-Structured LDPC Codes", <u>ISTCA-2001 Proc.</u> (Ambleside, England, 2001).	
S-AC		RICHARDSON et al., "Efficient Encoding of Low-Density Parity-Check Codes", <u>IEEE Trans. on Information Theory</u> , Vol. 47, No. 2 (Feb. 2001), pp. 638-656.	

Examiner Signature		Date Considered	6-27-06
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Substitute for form 1448/PTO		Complete if Known	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)		Application Number	10/724,280
		Filing Date	November 28, 2003
		First Named Inventor	Hocavar
		Art Unit	2133
		Examiner Name	
Sheet 2 of 2	Attorney Docket Number	TI-35252	

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S-AC		ZHANG et al., "VLSI Implementation-Oriented (3,k)-Regular Low-Density Parity-Check Codes", IEEE Workshop on Signal Processing Systems (Sept.2001), pp.25-36.	

Examiner Signature		Date Considered	6-27-06
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